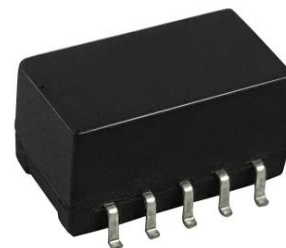


FEATURES:

- Wide input voltage input, regulated output
- Short-circuit protection
- Non isolated
- Working temperature: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
- No additional components required
- Stable performance and high reliability (MTBF $\geq$ 2000K hours)
- Industry standard pin-out
- Flame-retardant case to meet UL94V-0 requirements
- SMD package



Selection Guide

Part No.	INPUT		OUTPUT			Efficiency (%/Typ) Vin Min	Efficiency (%/Typ) Vin Max
	Normal (Vdc)	Range (Vdc)	Voltage (Vdc)	Adjust Range(m A)	current (mA)		
K7803T-500R3	12	4.5-28	3.3	1.8-5.5	500	90	75
K7805T-500R3	12	6-28	5	2.5-8	500	94	81
K7809T-500R3	24	11-28	9	3-11.5	500	95	87
K7812T-500R3	24	14-28	12	4.5-13.5	500	95	90
K7815T-500R3	24	17-28	15	4.5-15.5	500	96	92

customized accepted ,pls contact sales for details

Input Specifications

Input Filter	Capacitive Filter	
Ctrl	NONE	
	NONE	
Hot Plug	Unavailable	

Output Specifications

Item	Typ	Max	Test Conditions
Voltage Accuracy	$\pm 2\%$	$\pm 3\%$	Input voltage range at full load
Line Regulation	$\pm 0.2\%$	$\pm 0.4\%$	Input voltage range at full load
Load Regulation	$\pm 0.3\%$	-	Nominal Input ,10% to 100% load
Ripple&Noise	50mVp-p	75mVp-p	20MHz bandwidth

General Specifications

Operating Temperature	$-40 \sim +85^{\circ}\text{C}$	
Storage Temperature	$-55 \sim +125^{\circ}\text{C}$	
Storage Humidity	$<95\% \text{RH}$	Non-condensing
Cooling Method	Free air convection	
Case Material	Black plastic; flame-retardant and heat-resistant (UL94V-0)	
Weight	1.3g (Typ)	

**Unless specified, otherwise all other parameters are tested under the following conditions: nominal input voltage, pure resistive load, 25°C room temperature environment.

Dimensions and Recommended Layout



Pin	Mark		
2	Vin		
5	Vout		
6	Vadj		
7	GND		
9	GND		
10	ON/OFF		

The diagram illustrates a differential-mode signal path. On the left, the input is a differential pair with terminals labeled V_{in} and GND . A coupling capacitor C_{in} is connected between these two lines. The signal then passes through two identical blocks labeled DC , which represent differential-mode common-mode decoupling. The output is a differential pair with terminals labeled $+V_o$ and $-V_o$, and a common-mode output labeled $0V$. Coupling capacitors C_{out} are connected between the output lines and the common-mode output line.

Vin	Cin	Vout	Cout	
5VDC	4.7uF/16V	±3.3/5VDC	4.7uF/16V	
		±9VDC	2.2uF/16V	
		±12VDC	1uF/25V	
		±15/±24VDC	1uF/50V	

1. Input current: Ensure that the output current of the power supply meets the instantaneous starting current of the power module (that is, twice the average input current of the power module).
2. Output load requirements: Avoid no-load use. When the actual power consumption of the load is less than 10% of the rated output power of the module or no load occurs, connect an external resistance to the output end (the sum of the external resistance and the load power is greater than or equal to 10% of the rated load) or select a module with a smaller rated power.

3. The external capacitance of the output end should not be too large; otherwise, the module may be overcurrent or poorly started. For details, see the external capacitance recommendation table.
4. External LC filter circuit can be connected for occasions with high ripple noise requirements.

*The final interpretation right of the product belongs to ECCO ELECTRONICS.